Terasic Trex-C1 Board Pin Table

Pin	Memory	CF_Card	VGA	TV	SEG7_LED	KEY_DIP	JP1_JP2	Clock_CPLD	Audio	PS/2_RS232
2						KEY[7]				
4						KEY[6]				
6						KEY[5]				
7						KEY[4]				
8						KEY[3]				
11					LED[1]					
12						KEY[2]				
13						KEY[1]				
14						KEY[0]				
15	SD_DATA[0]									
16	SD_DATA[1]									
17	SD_DATA[2]									
18	SD_DATA[3]									
19	SD_DATA[4]									
20	SD_DATA[5]									
21	SD_DATA[6]									
28								OSC_50		
38	SD_CLK									
41	SD_DATA[7]									
42	SD_LDQM									
43	SD_WE_N									
44	SD_CAS_N									
45	SD_RAS_N									
46	SD_CS_N									
47	SD_BA_0									
48	SD_DATA[15]									
49	SD_DATA[14]									
50					LED[0]					
53	SD_DATA[13]									
54	SD_DATA[12]									
56	SD_DATA[11]									
57	SD_DATA[10]									
58	SD_DATA[9]									
59	SD_DATA[8]									
60	SD_UDQM									
61	SD_CKE									
62	SD_ADDR[11]									
63	SD_ADDR[9]									
64	SD_ADDR[8]									
65	SD_ADDR[7]									
66	SD_ADDR[6]									
67	SD_ADDR[5]									
68	SD_ADDR[4]									
73					LED[7]					
75	SD_BA_1									
76	SD_ADDR[10]									
77	SD_ADDR[0]									
78	SD_ADDR[1]									
79	SD_ADDR[2]									
80	SD_ADDR[3]									
81		CF_CS0_N								
82		CF_CS1_N								
83	FL_CE_N									
84	FL_ADDR[2]	CF_DD[3]								
85	FL_ADDR[3]	CF_DD[11]								
86	FL_ADDR[4]	CF_DD[4]					JP2[35]			
87	FL_ADDR[5]	CF_DD[12]					JP2[34]			
88	FL_ADDR[6]	CF_DD[5]					JP2[33]			
94	FL_ADDR[7]	CF_DD[13]					JP2[32]			
95	FL_ADDR[8]	CF_DD[6]					JP2[31]			
96	FL_ADDR[18]	CF_DD[14]					JP2[30]			
97	FL_ADDR[19]	CF_DD[7]					JP2[29]			
98	FL_RST_N	CF_DD[15]					JP2[28]			
99	FL_WE_N	CF_IOR_N					JP2[27]			
100	FL_ADDR[20]	CF_IOW_N					JP2[26]			
101	FL_ADDR[9]	CF_IRQ								
102	FL_ADDR[10]	CF_RST					JP2[25]			
103	FL_ADDR[11]						JP2[24]			
104	FL_ADDR[12]	CF_IORDY					JP2[23]			
105	FL_ADDR[13]	CF_DA[2]					JP2[22]			
106	FL_ADDR[14]	CF_DA[1]					JP2[21]			
108					LED[6]					
113	FL_ADDR[15]	CF_REG					JP2[20]			
114	FL_ADDR[16]	CF_DA[0]					JP2[19]			
115	FL_ADDR[1]	CF_DD[0]					JP2[18]			

Terasic Trex-C1 Board Pin Table

Pin	Memory	CF_Card	VGA	TV	SEG7_LED	KEY_DIP	JP1_JP2	Clock_CPLD	Audio	PS/2_RS232
116	FL_OE_N	CF_DD[1]					JP2[17]			
117	FL_DQ[0]	CF_DD[8]					JP2[16]			
118	FL_DQ[1]	CF_DD[2]					JP2[15]			
119	FL_DQ[2]	CF_DD[9]					JP2[14]			
120	FL_DQ[3]	CF_IO16					JP2[13]			
121	FL_DQ[4]	CF_DD[10]					JP1[35]			
122	FL_DQ[5]						JP1[34]			
123	FL_DQ[6]						JP1[33]			
124	FL_DQ[7]						JP1[32]			
125	FL_ADDR[0]						JP1[31]			
126	FL_ADDR[17]						JP1[30]			
128							JP1[29]			
131					LED[5]					
132							JP1[28]			
133							JP1[27]			
134							JP1[26]			
135							JP1[25]			
136							JP1[24]			
137							JP1[23]			
138							JP1[22]			
139							JP1[21]			
140							JP1[20]			
141							JP1[19]			
153								OSC_27		
158							JP1[18]			
159							JP1[17]			
160							JP1[16]			
161							JP1[15]			
162							JP1[14]			
163							JP1[13]			
164							JP1[12]			
165							JP1[11]			
166							JP1[10]			
167							JP1[9]			
168							JP1[8]			
169							JP1[7]			
170					LED[4]					
173							JP1[6]			
174							JP1[5]			
175							JP1[4]			
177							JP1[3]			PS2_DIN
178							JP1[2]			PS2_LAT
179							JP1[1]			PS2_DATA
180							JP1[0]			PS2_CLK
181						SW[3]	JP2[12]			
182						SW[2]	JP2[11]			
183						SW[1]	JP2[10]			
184						SW[0]	JP2[9]			
185					SEG7[7]		JP2[8]			
186					SEG7[6]		JP2[7]			
187					SEG7[5]		JP2[6]			
188					SEG7[4]		JP2[5]			
193					LED[3]					
195					SEG7[3]		JP2[4]			
196					SEG7[2]		JP2[3]			
197					SEG7[1]		JP2[2]			
198					SEG7[0]		JP2[1]			
199							JP2[0]			
200					COM[3]					
201					COM[2]					
202					COM[1]					
203					COM[0]					
204										UART_TXD
205										UART_RXD
206			HSYNC_N	HSYNC_N						
207			VSYNC_N	VSYNC_N						
213			BLUE[3]	YC[7]						
214			BLUE[2]	YC[6]						
215			BLUE[1]	YC[5]						
216			BLUE[0]	YC[4]						
217			GREEN[3]	YC[3]						
218			GREEN[2]	YC[2]						
219			GREEN[1]	YC[1]						
220			GREEN[0]	YC[0]						
221				TVRES						

Terasic Trex-C1 Board Pin Table

Pin	Memory	CF_Card	VGA	TV	SEG7_LED	KEY_DIP	JP1_JP2	Clock_CPLD	Audio	PS/2_RS232
222			RED[3]	TVNP						
223			RED[2]							
224			RED[1]							
225			RED[0]							
226									AUD_BCK	
228					LED[2]					
233									AUD_LRCK	
234									AUD_DATA	
235								TDI		
236								TCS		
237								TCK		
238								TDO		